

## APPLICATION DATA SHEET

### APPLICATION INFORMATION

Filing Date:: 03/24/04  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: Method For Monitoring And Improving  
Integrated Circuit Fabrication  
Using FPGAs  
Attorney Docket Number:: X-1437 US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 5  
Small Entity?:: No  
Petition included?:: No

### APPLICANT INFORMATION

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Joe  
Middle Name:: W.  
Family Name:: Zhao  
Street:: 1639 Via Cortina  
City:: San Jose  
State or Province:: CA  
Postal or Zip Code:: 95120

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Xiao-Yu  
Family Name:: Li  
Street:: 3481 Cowper Street  
City:: Palo Alto

State or Province:: CA  
Postal or Zip Code:: 94306  
  
Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Feng  
Family Name:: Wang  
Street:: 2978 Briona Court  
City:: San Jose  
State or Province:: CA  
Postal or Zip Code:: 95124

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Zhi-Min  
Family Name:: Ling  
Street:: 11521 Sunrise Spring Court  
City:: Cupertino  
State or Province:: CA  
Postal or Zip Code:: 95014

#### **CORRESPONDENCE INFORMATION**

Correspondence Customer Number:: 24309

#### **REPRESENTATIVE INFORMATION**

<b>Representative Customer Number::</b>	<b>24309</b>	
---	--------------	--

**ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124